

***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

Applicant: B. Mark Hirst  
Title: SNUBBER CIRCUIT  
Appl. No.: 10/780,927  
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Examiner: Patel, Rajnikant B.  
Art Unit: 2828

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**CONCISE EXPLANATION OF RELEVANCE**

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**DE10045093**

The invention relates to the minimization of power losses in the energy supply of a control circuit for a power transistor (T), whereby a circuit component (SNT), or another switching transformer means, is electrically connected in parallel with a capacitor (C1), the output therefrom is electrically connected to a feed voltage capacitor (C>S<) and said component transforms the input energy to the C>S< voltage level. The R>L< load (control circuit) is thus adequately supplied and only the absolutely necessary balancing current (I1) flows, due to the balancing resistance R>S< in a power transistor switch (T).

Respectfully submitted,

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